VLSI TRAINER KIT

SPECIFICATIONS:

ALS-SDA-FPGA-03 TRAINER ALS-SDA-FPGA-03 TRAINER is a tool for understanding the capabilities of an Industry Standard FPGA. 10 MHz clock and one of four different clocks (5MHz, 1MHz, 500KHz, 100KHz The board contains various interface options including Switches, LEDs, LCD, Seven segment display, Key matrix, Relay, Buzzer.

Traffic light simulator.

DC motor interface.

16 inputs using DIP switches. 16 outputs through output ports of FPGA connected to LEDsν 16x2 Alpha–Numeric LCD display with back-light.

Four-digit 7-segment displays 4x4 key matrix) On-board Buzzer. On-board DPDT Relay.

On-board Traffic Light Simulator.

On-board DC motor interface with two-pin connection to external DC motor.

On-board multiple DC supply voltage generator.

26-PIN FRC Cable for connecting to ALS standard interfaces like ADC, DAC, Elevator etc.

A number of sample programs to demonstrate the interfacing capabilities.

For details visit our website: www.hitechelectronicspune.com